Computer Fundamental and Microprocessors Quiz Questions and Answers Pdf

1. The contents of accumulator in an 8085 microprocessor are altered after the execution of the instruction?

   a. CMPC

   b. CP 13 A

   c. ANISC

   d. ORAA

   Ans: C

2. Which one of the following interrupts is both level and edge sensitivity?

   a. RST 7.5

   b. RST 5.5

   c. TRAP

   d. INTR

   Ans: C

3. In case the code is position dependent, the most suitable addressing mode is

   a. Direct mode

   b. Indirect mode

   c. Relative mode

   d. Indexed mode

   Ans: C

4. What type of network is the Internet?

   a. Circuit switched network

   b. Message switched network

   c. Packed switched network

   d. Cell switched network

   Ans: C

5. The following registers holds the instruction before it goes to the decode

   a. Control register

   b. Accumulator

c. Address register

d. Data register

As: A

6. The serial connection of interrupts lines for establishing hardware priority is known as

a. Daisy chaining priority

b. Parallel priority

c. Polling

d. Serial priority

Ans: A

7. In a DRAM

a. Periodic refreshing is not required

b. Information is stored in a capacitor

c. Information is stored in a latch

d. Both read and write operations can be performed simultaneously

Ans: B

8. Which block replacement algorithm is not generally used in cache operation?

a. LIFO

b. FIFO

c. LRU

d. Random

Ans: A

9. The principle of locality of reference justifies the use of

a. Interrupts

b. DMA

c. Virtual memory

d. Cache memory

Ans: D

10. The address bus of Intel 8085 is 16 bits and hence the memory which can be accessed by this address bus is

a. 1 K Byte

b. 16 K Bits

c. 32 K Bits

d. 64 K Bytes

Ans: D